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| ACTIVATING | 371226 |
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| DISPLAY | 2077926 |
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| ELEMENT | 3704304 |
| ELEMENTS | 3455371 |
| (23 AND (ACTIVATING SAME (DISPLAY ADJ ELEMENT))) .PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD. | 2 |
| (L23 AND ACTIVATING SAME DISPLAY ADJ ELEMENT) .PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD. | 2 |

Database:

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Search:

L23 and activating same display adj
 element

Search History

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| | <i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i> | | |
| <u>L25</u> | L23 and activating same display adj element | 2 | <u>L25</u> |
| <u>L24</u> | L23 and refresh | 4 | <u>L24</u> |
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| <u>L22</u> | l18 and row adj address same column adj address | 0 | <u>L22</u> |
| <u>L21</u> | l18 and row adj address adj input same column adj address adj input | 0 | <u>L21</u> |
| <u>L20</u> | L18 and display\$1 | 13 | <u>L20</u> |
| <u>L19</u> | L18 and refresh adj rate\$1 | 2 | <u>L19</u> |
| <u>L18</u> | column adj address same column adj electrode same row adj address same row adj electrode | 34 | <u>L18</u> |
| <u>L17</u> | L16 and row adj address same row adj electrode | 41 | <u>L17</u> |
| <u>L16</u> | column adj address same column adj electrode | 63 | <u>L16</u> |
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| SWITCH | 2117718 |
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| PIXEL | 304016 |
| PIXELS | 226408 |
| DISPLAY | 2077926 |
| DISPLAYS | 603450 |
| ELEMENT | 3704304 |
| (LOGIC ADJ CONTROLLED ADJ SWITCH SAME (PIXEL OR DISPLAY ADJ ELEMENT)).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD. | 2 |

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Search:

L40



Search History

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| <u>L35</u> | I33 and display\$1 | 7 <u>L35</u> |
| | <i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i> | |
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| <u>L21</u> | row adj address same column adj address same addressable adj latch | 1 <u>L21</u> |
| <u>L20</u> | L19 and row adj address same column adj address and addressable adj latch | 8 <u>L20</u> |
| <u>L19</u> | L15 and row adj address same column adj address | 2825 <u>L19</u> |
| <u>L18</u> | addressable adj latch and row adj address adj input and column adj address adj input | 1 <u>L18</u> |
| <u>L17</u> | addressable adj latch same row adj address adj input same column adj address adj input | 1 <u>L17</u> |
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| <u>L10</u> | pixel same addressable adj latch | 2 <u>L10</u> |
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| <u>L4</u> | L2 and logic adj control and switch | 1 | <u>L4</u> |
| <u>L3</u> | L2 and logic adj control adj switch | 0 | <u>L3</u> |
| <u>L2</u> | row adj address same row adj electrode same column adj address same column adj electrode | 34 | <u>L2</u> |
| <u>L1</u> | row adj address same row adj electrode same column adj address same column adj electrode | 0 | <u>L1</u> |

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